



Electrical Engineering and Embedded Systems (Master)

Master of Engineering

Module Manual

lt. SP0 vom 31.03.2022
Valid from: SoSe23



Content Modules

Master studies

Advanced Mathematics	
Communication 1	
Communication 2	
Circuit & Systems 1	
Signalprocessing 1	
Embedded Control	
Embedded Computing	
Master Thesis	
Computer Vision	
Lidar and Radar Systems	
SW-HW-Design	
Computer Architecture	
Advanced Control Systems	

Program Objectives

The aim of the Master's degree programme "Electrical Engineering and Embedded Systems" is to train young people with a versatile interest in technology who have the basic knowledge (the Bachelor's degree is a prerequisite) to familiarise themselves with special areas in the subject area of electrical engineering and embedded systems. The education includes the teaching of specialised knowledge, but also the development of social skills that make working in groups productive. In addition, the degree programme teaches methods for familiarising oneself with complex contexts and for systematic problem solving. The graduates' field of activity ranges from research, industry and the service sector to public service.

The requirements for engineers in electrical engineering and embedded systems are very diverse. The degree programme therefore aims to convey the subject-specific technical skills in the required breadth and depth. Key qualifications such as language skills, knowledge of project management as well as communication skills and time management are to be taught.

Connection of the modules

The subject-specific and methodical mathematical fundamentals are taught in the first semester. In the degree programme "Electrical Engineering and Embedded Systems", special emphasis is placed on a well-founded and broad-based basic education in mathematics (beyond the Bachelor's level). This comprises the Mathematics module.

The degree programme has two profiles: Autonomous Driving and Internet of Things (IoT), which are covered by the common subjects and specialisations.

The embedded systems essentially consist of computing with Circuit & Systems 1 and Computer Architecture (profile IoT) as well as signal processing (after the sensors) with Signalprocessing 1 and Signalprocessing 2 (as an elective), communication with the environment or system internally with Communication 1 and Communication 2, as well as control engineering with Advanced Control Systems and Embedded Control and finally the calculation and control of the system with Embedded Computing.

The two specialisations Autonomous Driving and IoT allow a deeper insight into the respective areas. The curriculum is rounded off by the project work and the Master's thesis with associated seminar. These modules enable a further individual focus and promote the acquisition of key competences such as teamwork, self-organisation and project management.

Through its modules, the degree programme thus provides an appropriate education in terms of breadth and depth for the demanding professional fields of graduates with an M. Eng. degree in Electrical Engineering and Embedded Systems.

COURSE CONTENTS

ELECTRICAL ENGINEERING AND EMBEDDED SYSTEMS

SEM.	MODULE OVERVIEW						ECTS
1	Embedded Computing	Circuit & Systems 1	Signal Processing 1	Profile Module 1 (Internet of Things or Autonomous Driving)	Mathematics		30
		5	5	5	10		
2	10	Communication 1 (Wireless Communication)	Communication 2 (Nearfield Communication)	Profile Module 2 (Internet of Things or Autonomous Driving)	Advanced Control Systems	Elective Module	30
		5	5	5	5	5	
3	Embedded Control	Master-Thesis incl. Colloquium					30
	5						25

■ Lecture subjects

■ Thesis

Advanced Mathematics

Course of study:	Electrical Engineering and Embedded Systems (Master)
Degree:	Master of Science (M.Sc.)
Modul number:	EMM01
Modul title:	Advanced Mathematics
Module responsible:	Prof. Dr. rer. nat. Wolfgang Ertel
Language of lecture:	english
Typ of module:	Mandatory module
Module Content:	<p>1 Linear Algebra (Repetition) - Video Lectures (Gilbert Strang)</p> <p>2 Computer Algebra - Gnuplot, a professional Plotting Software - Short Introduction to GNU Octave / MATLAB, Python</p> <p>3 Calculus - Selected Topics (Repetition) - Sequences and Convergence; - Series; - Continuity - Taylor Series - Differential Calculus in many Variables</p> <p>4 Statistics and Probability (Repetition) - Statistical Parameters - Probability Theory - Distributions - Random Numbers - Principal Component Analysis - Estimators</p> <p>5 Numerical Mathematics Fundamentals - Arithmetics on the Computer - Numerics of Linear Systems of Equations - Roots of Nonlinear Equations</p> <p>6 Function Approximation - Polynomial Interpolation - Spline interpolation - Method of Least Squares and Pseudoinverse - Singular Value Decomposition (SVD)</p> <p>7 Numerical Integration and Solution of Ordinary Differential Equations - Numerical Integration - Numerical Solution of Ordinary Differential Equations - Linear Differential Equations with Constant Coefficients</p>

Courses:	Advanced Mathematics for Engineers Advanced Mathematics for Engineers - Lab
Teaching and learning forms:	Lecture/Practical training
Prerequisites for participation:	Undergraduate Mathematics, e.g. Calculus (multidimensional), Linear Algebra, Statistics, Programming
Applicability of the module:	Mechatronics Electrical Engineering and Embedded Systems Informatik
Prerequisites allocation ECTS:	Portfolio with 80% weight of the written examination (K90) and 20% weight of the laboratory results (P).
ECTS credits:	10
Grading:	benotet
Workload:	30h / 1 ECTS
Duration of the module:	one semester
Frequency of offering:	Winter semester only
Literature:	J.W. Cheney and D. Kincaid. Numerical mathematics and computing. Thomson Brooks/Cole, 2007. J. Nocedal and S.J. Wright. Numerical optimization. Springer Verlag, 1999. S.M. Ross. Introduction to probability and statistics for engineers and scientists. Academic Press, 2009. G. Strang. Introduction to linear algebra. Wellesley Cambridge Press, 3rd edition, 2003. H. Schwarz: Numerische Mathematik, Teubner Verlag. M. Brill.: Mathematik für Informatiker. Hanser Verlag, 2001. W. Nehrllich: Diskrete Mathematik, Fachbuchverlag Leipzig.
Compulsory attendance:	no

Competence dimensions Advanced Mathematics

Knowledge and understanding: Deepening of individual components of knowledge

After successfully attending this course the graduates is able to solve mathematical problems arising in typical engineering tasks. Primary focus is on numerically solving on linear problems and on the statistical interpretation of results from measurements. In numerical mathematics, the focus is put on methods for function approximation from data, solution of equations, integration and solution of differential equations. Generation and test of random numbers are essential foundations of simulation and cryptography.

Use, application and generation of knowledge/art: Use and transfer

The graduates have broadened their knowledge in the following fields and are capable of reproducing this knowledge: High level programming languages with built in mathematical functions like Octave or Python will be used for the practical assignments (e.g. programming of algorithms). Absolventinnen und Absolventen skizzieren Ansatzpunkte für die Untersuchung der Effektivität bestehender IKS-Systeme sowie für das Design von High-Level-Controls in Unternehmen. Sie erläutern den aktuellen Stand zur Theorie der Konzernrechnungslegung und reflektieren deren Entstehungsgeschichte kritisch.

Communication and cooperation

Students work in small groups and discuss assignment problems. They uses role play techniques whereby one student presents a problem and the others serve as reviewers. Changing the roles and repeating this process helps finding solutions.

Scientific / artistic self-image and professionalism

The students know their own strength and weaknesses with respect to their study achievements.

Communication 1

Course of study:	Electrical Engineering and Embedded Systems (Master)
Degree:	Master of Science (M.Sc.)
Modul number:	EMM02
Modul title:	Communication 1
Module responsible:	Prof. Dr.-Ing. Frank Fechter
Language of lecture:	english
Typ of module:	Mandatory module
Module Content:	<p>A. Channel models for wireless communications</p> <p>1 Wave propagation</p> <p>2 Noise and Interference</p> <p>3 Spectrum issues</p> <p>B. Technology components of modern wireless systems⁴⁸</p> <p>4 Digital signal transmission</p> <p>5 Diversity</p> <p>6 Multi-Hop Networks</p> <p>7 Network Coding</p> <p>8 Cognitive radio</p> <p>9 Scheduling and rate control</p> <p>C. Wireless Systems</p> <p>10 Basics of Mobile Communication</p> <p>11 Global System for Mobile Communications (GSM)</p> <p>12 Long Term Evolution and System Architecture Evolution</p> <p>13 The Fifth Generation of Mobile Communication (5G)</p> <p>14. Beyond 5G</p>
Courses:	Wireless Communication
Teaching and learning forms:	Lecture, exercise, self-study
Prerequisites for participation:	Basics of Communication (Bachelor)
Applicability of the module:	Electrical Engineering and Embedded Systems
Prerequisites allocation ECTS:	K90
ECTS credits:	5
Grading:	graded
Workload:	150h
Duration of the module:	one semester
Frequency of offering:	Every semester

Literature:	<p>Dahlman, E. et al:4G: LTE/LTE-Advanced for Mobile Broadband. Academic Press, 2014 Dahlman, E. et al:4G: LTE-Advanced Pro and the road to 5G Academic Press, 2016. Molisch, A. F.: Wireless Communications. John Wiley & Sons, 2011 Holma H.; Toskala, A.: WCDMA for UMTS: HSPA Evolution and LTE. John Wiley & Sons, 2006 Holma H.; Toskala, A.: LTE for UMTS: HSPA Evolution to LTE-Advanced. John Wiley & Sons, 2011 Haykin, S.; Moher, M.: Modern Wireless Communications. Pearson Prentice Hall, 2005 Lescuyer, P.; Lucidarme, T.: Evolved Packet System (EPS) – The LTE and SAE Evolution of 3G UMTS. Wiley 2008. Larmo A. et al: The Link-Layer Design. IEEE Communications Magazine. April 2009 Wannstrom J.: LTE-Advanced. www.3gpp.org/technologies/keywords-acromyms/97-lte-advanced. Kosta, C.; Hunt, B.; Qudus, A ; Tafazolli, R.: On Interference Avoidance through Inter-Cell Interference Coordination (ICIC) based on OFDMA mobile systems. IEEE Communcations Surveys & Tutuorials. 2013 M.2083-0: IMT Vision – Framework and overall objectives of the future development of IMT for 2020 and beyond. ITU-R WP5D Recommendation. 09/2015 5GPPP Architecture Working Group. View on 5G Architecture. Version 2.0. December 2017. Retrieved from https://5g-ppp.eu/white-papers/. Retrieved 21.01.2022. System architecture for the 5G System (5GS); Stage 2 (Release 17). 3GPP. December 2021.</p>
Compulsory attendance:	no

Competence dimensions Communication 1

Knowledge and understanding: Broadening of prior knowledge

The graduates have broadened their knowledge in the following fields and are capable of reproducing this knowledge: Describe construction and functionality of modern mobile communication systems. They have understood the basic technologies, the architecture and protocols of selected wireless communication systems and they are able to describe it with his/her own words.

Use, application and generation of knowledge/art: Use and transfer

The graduates are capable of applying the knowledge they have acquired in the following fields: Mathematical methods for planning and optimization of communication systems.

The students are able to calculate mathematically selected problems of wireless communications, e.g. the range of radio transmission systems, diversity gain by multi-antenna systems and channel capacity of channels with relay nodes.

Communication and cooperation

The students work together in groups cooperative and responsible.

Scientific / artistic self-image and professionalism

The students know their own strength and weaknesses with respect to their study achievements.

Communication 2

Course of study:	Electrical Engineering and Embedded Systems (Master)
Degree:	Master of Science (M.Sc.)
Modul number:	EMM03
Modul title:	Communication 2
Module responsible:	Prof. Dr. rer. nat. Markus Pfeil
Language of lecture:	english
Typ of module:	Mandatory module
Module Content:	Basics of Nearfield Communication Transmission standards Protocols Applications
Courses:	Nearfield Communication
Teaching and learning forms:	Lecture and Project
Prerequisites for participation:	Basics of Communication (Bachelor)
Applicability of the module:	Electrical Engineering and Embedded Systems
Prerequisites allocation ECTS:	K90
ECTS credits:	5
Grading:	benotet
Workload:	Es wird von einem Workload von 30 Stunden je ECTS ausgegangen.
Duration of the module:	one semester
Frequency of offering:	Every semester
Literature:	Coscun et. al.: Near Field Communication (NFC): From Theory to Practice Wiley, 2012 Hendry :Near Field Communications Technology and Applications. Cambridge University Press, 2014
Compulsory attendance:	no

Competence dimensions Communication 2

Knowledge and understanding: Broadening of prior knowledge

The graduates have deepened their existing knowledge in the following areas and are capable of not only reproducing the corresponding contents but also of explaining them. They understand the underlying principles, the whys and wherefores: - describe the function of NFC systems with own words

Use, application and generation of knowledge/art: Scientific innovation

The graduates are capable of applying the knowledge they have acquired in the following fields and, additionally, of assessing their own approach to the theory-praxis-transfer and the result thereof: - evaluate and optimize NFC systems. The graduates can not only apply their knowledge and assess the application methods and / or results, they can also independently develop further research questions in the following fields: - create applications. Absolventinnen und Absolventen skizzieren Ansatzpunkte für die Untersuchung der Effektivität bestehender IKS-Systeme sowie für das Design von High-Level-Controls in Unternehmen. Sie erläutern den aktuellen Stand zur Theorie der Konzernrechnungslegung und reflektieren deren Entstehungsgeschichte kritisch. Absolventinnen und Absolventen skizzieren Ansatzpunkte für die Untersuchung der Effektivität bestehender IKS-Systeme sowie für das Design von High-Level-Controls in Unternehmen. Sie erläutern den aktuellen Stand zur Theorie der Konzernrechnungslegung und reflektieren deren Entstehungsgeschichte kritisch.

Communication and cooperation

Scientific / artistic self-image and professionalism

Circuit & Systems 1

Course of study:	Electrical Engineering and Embedded Systems (Master)
Degree:	Master of Science (M.Sc.)
Modul number:	EMM04
Modul title:	Circuit & Systems 1
Module responsible:	Prof. Dr.-Ing. Andreas Siggelkow
Language of lecture:	english
Typ of module:	Mandatory module
Module Content:	<ul style="list-style-type: none"> - ASIC-Design - Bus-Systems - Peripherals in a System-on-Chip (SoC) - Test and Debug of SoC - Principles of Micro-controller - MMU - Parallel Architectures
Courses:	System-on-Chip
Teaching and learning forms:	Lecture and Project
Prerequisites for participation:	Knowledge of computer architectures from Bachelor courses
Applicability of the module:	Electrical Engineering and Embedded Systems
Prerequisites allocation ECTS:	PF: <ul style="list-style-type: none"> - Requirements (10%) - 1. Specification (10%) - Simulation Sign-Off (10%) - Synthesis Sign-Off (10%) - Final Specification (40%) - Presentation (20%)
ECTS credits:	5
Grading:	benotet
Workload:	Es wird von einem Workload von 30 Stunden je ECTS ausgegangen.
Duration of the module:	one semester
Frequency of offering:	Every semester
Literature:	1. John L. Hennessy, David A. Patterson; Computer Architecture: A Quantitative Approach 2. David A. Patterson, John L. Hennessy; Computer Organization and Design: The Hardware/Software Interface
Compulsory attendance:	yes
Reason:	Laborarbeit

Competence dimensions Circuit & Systems 1

Knowledge and understanding: Broadening of prior knowledge

The graduates know the principle of computer architectures. They know how to design an ASIC.

Use, application and generation of knowledge/art: Use and transfer

The graduates can implement and organize a system on Chip with its peripherals. They can implement and use Test- and- Debug methods. Absolventinnen und Absolventen skizzieren Ansatzpunkte für die Untersuchung der Effektivität bestehender IKS-Systeme sowie für das Design von High-Level-Controls in Unternehmen. Sie erläutern den aktuellen Stand zur Theorie der Konzernrechnungslegung und reflektieren deren Entstehungsgeschichte kritisch.

Communication and cooperation

With the contents for the module, sustainable work, design and economics will be taught. It will be improved to a level, that it fits to the needs of companies. The intercultural competence of the graduates will be developed by

- international tandem teams
- mixed teams in the labs
- mixed teams for projects and seminars

Scientific / artistic self-image and professionalism

Signalprocessing 1

Course of study:	Electrical Engineering and Embedded Systems (Master)
Degree:	Master of Science (M.Sc.)
Modul number:	EMM06
Modul title:	Signalprocessing 1
Module responsible:	Prof. Dr.-Ing. Lothar Berger
Language of lecture:	english
Typ of module:	Mandatory module
Module Content:	Deterministic Continuous Signals - Laplace- and Fourier Transforms - Continuous Systems and Circuits for Signal Processing - Stability Issues - Nodal Admittance Method - MATLAB - OP-Amp Circuits - Stochastic Signals - Noise Analysis of OP-Amps - Integrated Lab Exercises
Courses:	Signalprocessing 1 Signalprocessing 1 Lab
Teaching and learning forms:	Lecture and Laboratory / practical course
Prerequisites for participation:	Bachelor knowledge
Applicability of the module:	Electrical Engineering and Embedded Systems
Prerequisites allocation ECTS:	K90
ECTS credits:	5
Grading:	graded
Workload:	150 h
Duration of the module:	one semester
Frequency of offering:	Winter semester only
Literature:	[Ghausi] Ghausi, Laker Modern Filter Design, Prentice-Hall, 1981 [Horowitz] Paul Horowitz, W. Hill The Art of Electronics, Cambridge University Press [Cooper] Cooper G. R., McGillem C. D., Probabilistic Methods of Signal an System Analysis, CBS 1986 [Doe] Doetsch, G. Anleitung zum praktischen Gebrauch der Laplace-Transformation. Oldenbourg, 1989
Compulsory attendance:	no

Competence dimensions Signalprocessing 1

Knowledge and understanding: Deepening of individual components of knowledge

The students are able to explain digital filters, stability, and signals.

Use, application and generation of knowledge/art: Scientific innovation

The students are able to explain digital filters, stability, and signals. Absolventinnen und Absolventen skizzieren Ansatzpunkte für die Untersuchung der Effektivität bestehender IKS-Systeme sowie für das Design von High-Level-Controls in Unternehmen. Sie erläutern den aktuellen Stand zur Theorie der Konzernrechnungslegung und reflektieren deren Entstehungsgeschichte kritisch.

Communication and cooperation

With the contents for the module, sustainable work, design and economics will be taught. It will be improved to a level, that it fits to the needs of companies.

The intercultural competence of the graduates will be developed by

- international tandem teams
- mixed teams in the labs
- mixed teams for projects and seminars

Scientific / artistic self-image and professionalism

The graduates understand specifications for analog circuits for signal processing. Thus being able to design new circuits based on the basic circuits and methods known in theory and in practice from the teaching module.

Embedded Control

Course of study:	Electrical Engineering and Embedded Systems (Master)
Degree:	Master of Science (M.Sc.)
Modul number:	EMM09
Modul title:	Embedded Control
Module responsible:	Prof. Dr.-Ing. Lothar Berger
Language of lecture:	english
Typ of module:	Mandatory module
Module Content:	Attendees are first given an overview of recent research in control engineering, applicable for industrial process embedded control, like optimal control, nonlinear robust control, data driven control, application of Kalman filter, and machine learning control. Then attendees group in teams, and each team selects one method for working upon. Each team collaborates in researching literature, working out implementation, visualization, and operation of selected process model and embedded control algorithm, within the lab, either through simulation or embedded design; and prepares a seminar paper and seminar presentation. This seminar presentation is then given by all members of a team consecutively, to the audience of all attendees.
Courses:	Embedded Control Seminar Embedded Control Lab
Teaching and learning forms:	Seminar; Lab - or - E-Learning: Seminar; Homework: Practical work
Prerequisites for participation:	Advanced Control Systems
Applicability of the module:	Master Thesis
Prerequisites allocation ECTS:	RPA (PF: 50% PA graded, 50% R graded): Practical work, documented by a seminar paper and presentation
ECTS credits:	5
Grading:	benotet
Workload:	Presence: 48h, Self-study: 102h - or - Online: 24h, Self-study: 102h, Homework: 24h
Duration of the module:	one semester
Frequency of offering:	Winter semester only
Literature:	
Compulsory attendance:	no

Competence dimensions Embedded Control

Knowledge and understanding: Knowledge Comprehension

Attendees learned about recent research in control engineering, applicable for industrial process embedded control, like optimal control, nonlinear robust control, data driven control, application of Kalman filter, and machine learning control.

Use, application and generation of knowledge/art: Scientific innovation

Attendees learned researching literature, working out implementation, visualization, and operation of industrial process model and embedded control algorithm, and preparing a seminar paper and seminar presentation. Absolventinnen und Absolventen skizzieren Ansatzpunkte für die Untersuchung der Effektivität bestehender IKS-Systeme sowie für das Design von High-Level-Controls in Unternehmen. Sie erläutern den aktuellen Stand zur Theorie der Konzernrechnungslegung und reflektieren deren Entstehungsgeschichte kritisch.

Communication and cooperation

Attendees learned presenting scientific results in a structured manner, utilizing adequate terminology, and gained experience in intercultural team collaboration and communication.

Scientific / artistic self-image and professionalism

Attendees learned developing scientific results through adequate proceeding and planning.

Embedded Computing

Course of study:	Electrical Engineering and Embedded Systems (Master)
Degree:	Master of Science (M.Sc.)
Modul number:	EMM10
Modul title:	Embedded Computing
Module responsible:	Prof. Dr. rer. nat. Markus Pfeil
Language of lecture:	english
Typ of module:	Mandatory module
Module Content:	<ul style="list-style-type: none"> - Embedded Systems in motor management, ABS, medical devices and its increasing programming needs. - Modeling of embedded systems (Cyber-Physical Systems) - Functions of 32-bit micro controllers (ARM), interface functions, its programming under Linux - Implementation of operating systems on microcontrollers
Courses:	Embedded Computing Embedded Computing Lab Embedded Project
Teaching and learning forms:	Lecture and Laboratory / practical course and Project Lecture with integrated applications, development and programming of functions for embedded systems, project management (project idea, realization, presentation)
Prerequisites for participation:	Bachelor knowledge
Applicability of the module:	Electrical Engineering and Embedded Systems Mechatronics
Prerequisites allocation ECTS:	PF
ECTS credits:	10
Grading:	graded
Workload:	10 ECTS Embedded Computing 150 h (60 h Lecture, 90 h Homework) Embedded Computing Lab 60 h (30 h Lecture, 30 h Homework) Embedded Project 90 h (30 Lecture, 60 h Homework)
Duration of the module:	two semester
Frequency of offering:	Every semester
Literature:	B. P. Douglas; "Real-Time UML", Second Edition. Addison Wesley Longman, Inc., 2000. P. Marwedel; "Embedded System Design", Springer Verlag, 2006. D. Abbott; "Linux for Embedded and Real-time Applications", Elsevier Science, 2003
Compulsory attendance:	no

Competence dimensions Embedded Computing

Knowledge and understanding: Knowledge Comprehension

The graduates have broadened their knowledge in the following fields and are capable of reproducing this knowledge:

- Mechatronic and electrical engineering
- Model and simulate mechatronic systems
- Construct electrical and IT components

Use, application and generation of knowledge/art:

Absolventinnen und Absolventen skizzieren Ansatzpunkte für die Untersuchung der Effektivität bestehender IKS-Systeme sowie für das Design von High-Level-Controls in Unternehmen. Sie erläutern den aktuellen Stand zur Theorie der Konzernrechnungslegung und reflektieren deren Entstehungsgeschichte kritisch.

Communication and cooperation

With the contents for the module, sustainable work, design and economics will be taught. It will be improved to a level, that it fits to the needs of companies. The intercultural competence of the graduates will be developed by

- international tandem teams
- mixed teams in the labs
- mixed teams for projects and seminars

Scientific / artistic self-image and professionalism

In the course of their study, the graduates have already reached a level of knowledge and understanding that enables them to analyze not only simple but also complex interactions. On this basis, they are capable of independently identifying scientific or practice-related issues in the following fields:

- mechatronic questions
- model and simulate mechatronic systems
- construct electrical and IT components

- present mechatronic projects

Master Thesis

Course of study:	Electrical Engineering and Embedded Systems (Master)
Degree:	Master of Science (M.Sc.)
Modul number:	EMM12
Modul title:	Master Thesis
Module responsible:	Prof. Dr.-Ing. Lothar Berger
Language of lecture:	english
Typ of module:	Mandatory module
Module Content:	Students should prove their knowledge gained from theoretical and practical lectures on an engineering project or a research transaction. Working methodology shall be tailored to the needs of the employer, including sustainability, design and economic aspects.
Courses:	-Master Thesis activity -Master Thesis report -Master Thesis colloquium
Teaching and learning forms:	Engineering and/or research experience
Prerequisites for participation:	The Master's thesis can only be commenced if all courses and related coursework required for semesters EMM1 and EMM2 have been completed, corresponding to at least 50 credit points.
Applicability of the module:	
Prerequisites allocation ECTS:	-Delivery of Master Thesis report -Presentation of the results in a colloquium public to all members of the university.
ECTS credits:	25
Grading:	graded
Workload:	The Master Thesis shall have a duration of 6 months. It will be assessed and graded by one professor lecturing at the Hochschule Ravensburg-Weingarten - University of Applied Sciences as 1st examiner, and a 2nd examiner holding appropriate Master's degree.
Duration of the module:	one semester
Frequency of offering:	Every semester
Literature:	
Compulsory attendance:	no

Competence dimensions Master Thesis

Knowledge and understanding: Knowledge Comprehension

The students are able to define, work on, evaluate and explain scientific topics.

Use, application and generation of knowledge/art: Scientific innovation

The students are able to define, work on, evaluate and explain scientific topics.

Communication and cooperation

The students can prove their knowledge achieved during their studies theoretically, practically and will be able to defend it. The students have to discuss throughout the duration of the thesis their work with others.

Scientific / artistic self-image and professionalism

The students see their own strength and weakness with respect to their studies and work on a scenario for their future as an engineer or researcher. The students are open to accept hints and ideas from colleagues. The students are able to design sustainable products.

Computer Vision

Course of study:	Electrical Engineering and Embedded Systems (Master)
Degree:	Master of Science (M.Sc.)
Modul number:	EMM13
Modul title:	Computer Vision
Module responsible:	Prof. Dr. rer. nat. Stefan Elser
Language of lecture:	english
Typ of module:	Elective module
Module Content:	<ol style="list-style-type: none"> 1. Brief introduction 2. The pinhole camera model 3. Recognition 4. Motion analysis 5. 3D reconstruction <p>We will take a look at both, traditional and machine learning algorithms in Computer Vision. For traditional algorithms, we will work with OpenCV. For machine learning algorithms, we will take a look at the TensorFlow Object Detection API or a comparable framework. As part of this course, you will implement or evaluate one of these algorithms using C++ or Python. The algorithms will either have to work on already recorded data (like the datasets above) or a given sensor.</p>
Courses:	7781 Computer Vision
Teaching and learning forms:	Vorlesung
Prerequisites for participation:	<p>Good understanding of mathematics in general.</p> <p>Good understanding of at least one programming language, preferable Python or C++.</p>
Applicability of the module:	Electrical Engineering and Embedded Systems
Prerequisites allocation ECTS:	PF consisting of 50% PA and 50% K60
ECTS credits:	5
Grading:	benotet
Workload:	approx. 50h for lectures, approx. 100h for self-study (preparation and follow-up, exam preparation)
Duration of the module:	one semester
Frequency of offering:	Summer semester only
Literature:	<p>Szeliski, Richard. "Computer vision: algorithms and applications." Springer Science & Business Media</p> <p>Janai, Joel, et al. "Computer vision for autonomous vehicles: Problems, datasets and state of the art.", arXiv:1704.05519v2</p>
Compulsory attendance:	no

Competence dimensions Computer Vision

Knowledge and understanding: Deepening of individual components of knowledge

Absolventinnen und Absolventen haben ihr Wissen auf folgenden Gebieten erweitert und können dieses Wissen auch wiedergeben:
Grundlegende Algorithmen der Computer Vision wie Object Detection, Motion Analysis, 3D Reconstruction.

Use, application and generation of knowledge/art: Use and transfer

Absolventinnen und Absolventen können das Wissen aus folgenden Themenbereichen praktisch anwenden:
Implementierung von Computer Vision Algorithmen und deren Evaluation. Absolventinnen und Absolventen skizzieren Ansatzpunkte für die Untersuchung der Effektivität bestehender IKS-Systeme sowie für das Design von High-Level-Controls in Unternehmen. Sie erläutern den aktuellen Stand zur Theorie der Konzernrechnungslegung und reflektieren deren Entstehungsgeschichte kritisch.

Communication and cooperation

Scientific / artistic self-image and professionalism

Lidar and Radar Systems

Course of study:	Electrical Engineering and Embedded Systems (Master)
Degree:	Master of Science (M.Sc.)
Modul number:	EMM14
Modul title:	Lidar and Radar Systems
Module responsible:	Prof. Dr. rer. nat. Stefan Elser
Language of lecture:	english
Typ of module:	Elective module
Module Content:	<ol style="list-style-type: none"> 1. Brief introduction 2. Radar sensors and signals 3. Radar: Velocity and distance measurement 4. Lidar sensors 5. Lidar: Distance measurement 6. Algorithms: simple object detection and tracking <p>We will focus on automotive applications but also take a look at other applications. As part of this course, you will use a sensor or already recorded data to implement your own algorithms or evaluate already existing functions using C++ or Python</p>
Courses:	7945 Lidar and Radar Systems
Teaching and learning forms:	Vorlesung
Prerequisites for participation:	<p>Good understanding of mathematics in general.</p> <p>Good understanding of at least one programming language, preferable Python, C or C++.</p>
Applicability of the module:	Electrical Engineering and Embedded Systems
Prerequisites allocation ECTS:	PF consisting of 50% PA and 50% K60
ECTS credits:	5
Grading:	benotet
Workload:	approx. 50h for lectures, approx. 100h for self-study (preparation and follow-up, exam preparation)
Duration of the module:	one semester
Frequency of offering:	Winter semester only
Literature:	<p>Merril Ivan Skolnik: "Introduction to radar systems", McGraw Hill Book Co.</p> <p>Christian Wolff: "Radartutorial", www.radartutorial.eu</p> <p>Feng, Di, et al. "Deep multi-modal object detection and semantic segmentation for autonomous driving: Datasets, methods, and challenges." IEEE Transactions on Intelligent Transportation Systems (2020).</p>
Compulsory attendance:	no

Competence dimensions Lidar and Radar Systems

Knowledge and understanding: Deepening of individual components of knowledge

Absolventinnen und Absolventen haben ihr Wissen auf folgenden Gebieten erweitert und können dieses Wissen auch wiedergeben:

Physikalische Grundlagen von Radar- und Lidar-Sensoren, grundlegende Algorithmen zur Signalauswertung, Geschwindigkeitsberechnung und Objekterkennung.

Use, application and generation of knowledge/art: Use and transfer

Absolventinnen und Absolventen können das Wissen aus folgenden Themenbereichen praktisch anwenden:

Implementierung und Evaluation einer Objekterkennung in Punktwolken. Absolventinnen und Absolventen skizzieren Ansatzpunkte für die Untersuchung der Effektivität bestehender IKS-Systeme sowie für das Design von High-Level-Controls in Unternehmen. Sie erläutern den aktuellen Stand zur Theorie der Konzernrechnungslegung und reflektieren deren Entstehungsgeschichte kritisch.

Communication and cooperation

Scientific / artistic self-image and professionalism

SW-HW-Design

Course of study:	Electrical Engineering and Embedded Systems (Master)
Degree:	Master of Science (M.Sc.)
Modul number:	EMM15
Modul title:	SW-HW-Design
Module responsible:	Prof. Dr.-Ing. Andreas Siggelkow
Language of lecture:	english
Typ of module:	Compulsory elective module
Module Content:	<ul style="list-style-type: none"> - Introduction to System-on-Chip - Requirements management - Writing a Specification - Modelling (SystemC and VHDL) of a SoC - Development of a SoC
Courses:	SW- and HW-Design
Teaching and learning forms:	Lecture and Project
Prerequisites for participation:	Bachelor knowledge of digital circuit design, Circuit & Systems 1, VHDL and C++
Applicability of the module:	SG: Electrical Engineering and Embedded Systems SG: Master Mechatronics SG: Master Informatik Module: Master-Thesis
Prerequisites allocation ECTS:	PF (50% Practical, 50% K60) Practical: <ul style="list-style-type: none"> - Requirements (10% of 50%) - 1. Specification (10% of 50%) - 1. Simulation Sign-Off (10% of 50%) - 2. Simulation Sign-Off (10% of 50%) - Final Specification (40% of 50%) - Presentation & Questions (20% of 50%) K60: <ul style="list-style-type: none"> - 50%
ECTS credits:	5
Grading:	graded PF (50% P, 50% K60)
Workload:	30 h per ECTS = 150 h in total 60 h for lectures 90 h for preparations

Duration of the module:	one semester
Frequency of offering:	Winter semester only
Literature:	<ol style="list-style-type: none"> 1. Arora, Mohit; The Art of Hardware Architecture: Design Methods and Techniques for Digital Circuits 2. Douglas Perry; VHDL : Programming By Example 3. Patrick Schaumont; A Practical Introduction to Hardware/Software Codesign 4. SystemC from the Ground Up
Compulsory attendance:	yes
Reason:	<ol style="list-style-type: none"> 1. Lab.- work 2. minimum 80 % 3. there will be dates for repetition of tasks (Because of Corona, the presence times will be reduced)

Competence dimensions SW-HW-Design

Knowledge and understanding: Deepening of individual components of knowledge

The graduates know what the specifics of a System-on-Chip are. They know how to model a complex electrical system (SoC).

Use, application and generation of knowledge/art: Use and transfer

The graduates will be able to develop a SoC and to describe a SoC with SystemC. They will be able to describe a SoC with VHDL and synthesize it. Absolventinnen und Absolventen skizzieren Ansatzpunkte für die Untersuchung der Effektivität bestehender IKS-Systeme sowie für das Design von High-Level-Controls in Unternehmen. Sie erläutern den aktuellen Stand zur Theorie der Konzernrechnungslegung und reflektieren deren Entstehungsgeschichte kritisch.

Communication and cooperation

The intercultural competence of the graduates will be developed by

- international tandem teams
- mixed teams in the labs
- mixed teams for projects and seminars

With the contents for the module, sustainable work, design and economics will be taught. It will be improved to a level, that it fits to the needs of companies.

Scientific / artistic self-image and professionalism

The graduates can organize an architecture and a team of a SW-HW project (System Architect).

Computer Architecture

Course of study:	Electrical Engineering and Embedded Systems (Master)
Degree:	Master of Science (M.Sc.)
Modul number:	EMM16
Modul title:	Computer Architecture
Module responsible:	Prof. Dr.-Ing. Andreas Siggelkow
Language of lecture:	english
Typ of module:	Compulsory elective module
Module Content:	<ul style="list-style-type: none"> - A general overview of computer architectures - Critical discussion of design criteria - Scalar architectures - Instruction level parallelism - Thread level parallelism - Memory hierarchies - ARM and RISC-V architecture
Courses:	Computer Architecture
Teaching and learning forms:	Lecture and Project
Prerequisites for participation:	Basics of computer architectures
Applicability of the module:	SG: Electrical Engineering and Embedded Systems Module: Master-Thesis
Prerequisites allocation ECTS:	PF or K90 PF in SS2023: 1.) Design of a RISC-V in VHDL (50%) 2.) K60 (50%)
ECTS credits:	5
Grading:	graded
Workload:	30 h per ECTS = 150 h in total 60 h for lectures 90 h for preparations
Duration of the module:	one semester
Frequency of offering:	Summer semester only
Literature:	J.L. Hennessy, D.A. Patterson; "Computer Architecture"; Morgan Kaufmann Publisher D.A. Patterson, J.L. Hennessy; "Computer Organization and Design"; Morgan Kaufmann Publisher
Compulsory attendance:	no

Competence dimensions Computer Architecture

Knowledge and understanding: Deepening of individual components of knowledge

The graduates know the difference of scalar and super-scalar architectures, they know how to evaluate the different kinds of caches and memory administrations.

Use, application and generation of knowledge/art: Scientific innovation

The graduates know the difference of scalar and super-scalar architectures, they know how to evaluate the different kinds of caches and memory administrations.

Absolventinnen und Absolventen skizzieren Ansatzpunkte für die Untersuchung der Effektivität bestehender IKS-Systeme sowie für das Design von High-Level-Controls in Unternehmen. Sie erläutern den aktuellen Stand zur Theorie der Konzernrechnungslegung und reflektieren deren Entstehungsgeschichte kritisch.

Communication and cooperation

The intercultural competence of the graduates will be developed by - international tandem teams

- mixed teams in the labs
- mixed teams for projects and seminars

With the contents for the module, sustainable work, design and economics will be taught. It will be improved to a level, that it fits to the needs of companies.

Scientific / artistic self-image and professionalism

The graduates can organize an architecture and a team of a CPU project (System Architect).

Advanced Control Systems

Course of study:	Electrical Engineering and Embedded Systems (Master)
Degree:	Master of Science (M.Sc.)
Modul number:	EMM17
Modul title:	Advanced Control Systems
Module responsible:	Prof. Dr.-Ing. Lothar Berger
Language of lecture:	english
Typ of module:	Mandatory module
Module Content:	Attendees are first given an introduction to analysis and modeling of dynamic systems - electrical, mechanical, and thermal. Then, design and optimization of single and multiple stage digital PID control is presented, as well as single-input and multi-input state control - without and with observer, optimal control, and model-predictive control. Finally, adaptive control methods are illustrated; based upon recursive parameter estimation, and neural nets. Within the complementary lab, attendees are educated to choose and implement suitable digital control methods for given dynamic systems - like mixer tank setup and balanced beam setup - utilizing computer-based tools like MATLAB/Simulink; as C programmed algorithms.
Courses:	Digital Control Digital Control Lab
Teaching and learning forms:	Lecture; Lab - or - E-Learning: Lessons, Exercises; Homework: Practical work
Prerequisites for participation:	Advanced Mathematics
Applicability of the module:	Simulation of Mechatronic Systems, Integration of Mechatronic Systems, Robotics, Embedded Control, Scientific Project, Master Thesis
Prerequisites allocation ECTS:	K60: Written examination; 60 minutes
ECTS credits:	5
Grading:	graded
Workload:	Presence: 60h, Self-study: 90h - or - Online: 36h, Self-study: 90h, Homework: 24h
Duration of the module:	one semester
Frequency of offering:	Summer semester only
Literature:	Script - or - lessons, exercises, and sample solutions; and complementary: Burns, R.S., Advanced Control Engineering, Butterworth-Heinemann Macia, N. F., Thaler, G. J.: Modeling and Control of Dynamic Systems, Cengage Learning Moudgalya, K. M.: Digital Control, Wiley Press, W. H., Teukolsky, S. A., Numerical Recipes in C, Cambridge
Compulsory attendance:	no

Competence dimensions Advanced Control Systems

Knowledge and understanding: Deepening of individual components of knowledge

Attendees learned about models of dynamic systems - electrical, mechanical, and thermal - and both classical control methods, like digital PID control, and advanced control methods, like state control without/with observer, model-predictive control, and adaptive control.

Use, application and generation of knowledge/art: Scientific innovation

Attendees learned to characterize, model, and simulate dynamic systems - electrical, mechanical, and thermal - and choose and implement suitable digital control methods, from both established methods, like digital PID control, and advanced methods, like state control without/with observer, optimal control, model-predictive control, and adaptive control. Attendees learned to assess these digital control methods, regarding effort, safety, and cost-effectiveness; and how to implement these, utilizing computer-based tools like MATLAB/Simulink; as C programmed algorithms. Through the complementary lab, attendees learned to choose and implement suitable digital control methods for given dynamical systems; like mixer tank setup and balanced beam setup. Absolventinnen und Absolventen skizzieren Ansatzpunkte für die Untersuchung der Effektivität bestehender IKS-Systeme sowie für das Design von High-Level-Controls in Unternehmen. Sie erläutern den aktuellen Stand zur Theorie der Konzernrechnungslegung und reflektieren deren Entstehungsgeschichte kritisch.

Communication and cooperation

Attendees learned about presenting and applying advanced digital control methods as a systems science; aimed at interdisciplinary projects; operated within a team of scientists, engineers, designers, and economists.

Scientific / artistic self-image and professionalism

Attendees learned about economical and ecological considerations in choosing, implementing, and optimizing advanced digital control methods for industrial processes.

Print date: 17.02.2023